



03/28/2006

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date

March 24, 2006

Denise Sheridan

Denise Sheridan

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/660,844

Confirmation No. : 7877

Applicant : Joseph M. Jeddelloh

Filed : September 12, 2003

Attorney Docket No.: 501321.01

Art Unit : 2184

Customer No. : 27,076

Examiner : Not yet assigned

Title : SYSTEM AND METHOD FOR ON-BOARD TIMING MARGIN TESTING OF  
MEMORY MODULES

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicant wishes to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449. Copies of the cited U.S. patents and U.S. patent application publications will not be submitted herewith in accordance with the waiver by the Office of the requirement under 37 C.F.R. § 1.98(a)(2)(i) for U.S. national patent applications filed after June 30, 2003. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicant's duty to disclose all information he is aware of which is believed relevant to the examination of the above-identified application, applicant believes that his invention is patentable.

Please acknowledge receipt of this Supplemental Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

DORSEY & WHITNEY LLP



Edward W. Bulchis  
Registration No. 26,847

EWB:sp

Enclosures:

Postcard  
Form PTO-1449

1420 Fifth Avenue, Suite 3400  
Seattle, WA 98101  
Telephone (206) 903-8800  
Facsimile (206) 903-8820

<h:\ip\clients\micron technology\1300\501321.01\501321.01 sids 032406.doc>

FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501321.01	APPLICATION NO. 10/660,844
O I P E JC25 INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT(S) <b>Joseph M. Jeddeloh</b>		
		FILING DATE September 12, 2002	GROUP ART UNIT Not yet assigned	

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AA	5,251,303	10/05/93	Fogg, Jr. et al.	395	275	
	AB	5,432,907	07/11/95	Picazo, Jr. et al.	395	200	
	AC	5,442,770	08/15/95	Barratt	395	403	
	AD	5,497,476	03/05/96	Oldfield et al.	395	439	
	AE	5,659,798	08/19/97	Blumrich et al.	395	846	
	AF	5,875,352	02/23/99	Gentry et al.	395	843	
	AG	6,061,263	05/09/00	Boaz et al.	365	51	
	AH	6,073,190	06/06/00	Rooney	710	56	
	AI	6,105,075	08/15/00	Ghaffari	710	5	
	AJ	6,137,709	10/24/00	Boaz et al.	365	51	
	AK	6,144,587	11/07/00	Yoshida	365	189.05	
	AL	6,167,465	12/26/00	Parvin et al.	710	22	
	AM	6,167,486	12/26/00	Lee et al.	711	120	
	AN	6,201,724 B1	03/13/01	Ishizaki et al.	365	49	
	AO	6,233,376 B1	05/15/01	Updegrove	385	14	
	AP	6,243,769 B1	06/05/01	Rooney	710	56	
	AQ	6,285,349 B1	09/04/01	Smith	345	147	
	AR	6,301,637 B1	10/09/01	Krull et al.	711	112	
	AS	6,327,642 B1	12/04/01	Lee et al.	711	120	
	AT	6,330,205 B2	12/11/01	Shimizu et al.	365	230.06	
	AU	6,347,055 B1	02/12/02	Motomura	365	189.05	
↓	AV	6,349,363 B2	02/19/02	Cai et al.	711	129	
	AW	6,356,573 B1	03/12/02	Jonsson et al.	372	46	
SS	AX	6,367,074 B1	04/02/02	Bates et al.	717	11	

EXAMINER <i>/Saqib Siddiqui/</i>	DATE CONSIDERED 06/22/2006
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\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

FORM PTO-1449 (REV.7-80)	OCT 27 2003 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501321.01	APPLICATION NO. 10/660,844
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT(S) <b>Joseph M. Jeddeloh</b>	
		FILING DATE September 12, 2002	GROUP ART UNIT Not yet assigned

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AY	6,373,777 B1	04/16/02	Suzuki	365	230.03	
	AZ	6,381,190 B1	04/30/02	Shinkai	365	230.03	
	BA	6,421,744 B1	07/16/02	Morrison et al.	710	22	
	BB	6,434,639 B1	08/13/02	Haghghi	710	39	
	BC	6,463,059 B1	10/08/02	Movshovich et al.	370	389	
	BD	6,470,422 B2	10/22/02	Cai et al.	711	129	
	BE	6,473,828 B1	10/29/02	Matsui	711	104	
	BF	6,477,614 B1	11/05/02	Leddige et al.	711	5	
	BG	6,477,621 B1	11/05/02	Lee et al.	711	120	
	BH	6,496,909 B1	12/17/02	Schimmel	711	163	
	BI	6,505,287 B2	01/07/03	Uematsu	711	170	
	BJ	6,539,490 B1	03/25/03	Forbes et al.	713	401	
	BK	6,552,564 B1	04/22/03	Forbes et al.	326	30	
↓	BL	6,587,912 B2	07/01/03	Leddige et al.	711	5	
	BM	6,594,713 B1	07/15/03	Fuoco et al.	710	31	
SS	BN	2002/0112119 A1	08/15/02	Halbert et al.	711	115	

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	BO							

**OTHER PRIOR ART** (Including Author, Title, Date, Pertinent Pages, Etc.)

SS	BP	Micron Technology, Inc., "Synchronous DRAM Module 512MB/1GB (x72, ECC) 168-PIN Registered FBGA SDRAM DIMM, Micron Technology, Inc., 2002, pp. 1-23.
	BQ	

EXAMINER	/Saqib Siddiqui/	DATE CONSIDERED	06/22/2006
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<b>O I P</b> <b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>		APPLICANT(S) <b>Joseph M. Jeddeloh</b>			
JAN 20 2004		FILING DATE <b>September 12, 2002</b>	GROUP ART UNIT <b>2184</b>		

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AA	6,067,262	05/23/00	Irrinki et al.	365	201	
SS	AB	6,246,618 B1	06/12/01	Yamamoto et al.	365	200	
SS	AC	6,590,816 B2	07/08/03	Perner	365	200	
SS	AD	2003/0043426 A1	03/06/03	Baker et al.	359	109	
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EXAMINER <i>/Saqib Siddiqui/</i>				DATE CONSIDERED <b>06/22/2006</b>			

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FORM PTO-1449 (REV.7-80)	FEB 26 2004	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501321.01	APPLICATION NO. 10/660,844
INFORMATION DISCLOSURE STATEMENT <small>(Use several sheets if necessary)</small>		APPLICANT(S) Joseph M. Jeddeloh		
		FILING DATE September 12, 2002	GROUP ART UNIT 2184	

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AA	5,818,844	10/06/98	Singh et al.	370	463	
	AB	6,272,609	08/07/01	Jeddeloh	711	169	
	AC	6,477,592	11/05/02	Chen et al.	710	52	
	AD	6,523,092	02/18/03	Fanning	711	134	
	AE	6,523,093	02/18/03	Bogin et al.	711	137	
	AF	6,622,227	09/16/03	Zumkehr et al.	711	167	
	AG	6,631,440	10/07/03	Jenne et al.	711	105	
	AH	2002/0144064	10/03/02	Fanning	711	144	
↓	AI	2003/0005223	01/02/03	Coulson et al.	711	118	
SS	AJ	2003/0229770	12/11/03	Jeddeloh	711	213	
	AK						
	AL						
	AM						
	AN						
	AO						
	AP						
	AQ						
	AR						
	AS						

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

SS	AT	Intel, "Intel 840 Chipset: 82840 Memory Controller Hub (MCH)", Datasheet, October 1999, pp. 1-178.
	AU	

EXAMINER  /Saqib Siddiqui/	DATE CONSIDERED  06/22/2006
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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO. 501321.01	APPLICATION NO. 10/660,844
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				FILING DATE September 12, 2002	GROUP ART UNIT 2184

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AA	6,462,978	10/08/02	Shibata et al.	365	63	
	AB						
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## EXAMINER

/Saqib Siddiqui/

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O I P E JUL 15 2004 P A T E N T & T R A D E M A R K I N F O R M A T I O N D I S C L O S U R E S T A T E M E N T (Use several sheets if necessary)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501321.01	APPLICATION NO. 10/660,844
				APPLICANT(S) Joseph M. Jeddeloh	
				FILING DATE September 12, 2003	GROUP ART UNIT 2184

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AA	5,544,319	08/06/96	Acton et al.	395	200.07	
	AB	5,822,255	10/13/98	Uchida	365	194	
	AC	5,875,454	02/23/99	Craft et al.	711	113	
	AD	6,033,951	03/07/00	Chao	438	253	
	AE	6,079,008	06/20/00	Clery, III	712	11	
	AF	6,186,400 B1	02/13/01	Dvorkis et al.	235	462.45	
	AG	6,243,831 B1	06/05/01	Mustafa et al.	714	24	
	AH	6,256,692 B1	07/03/01	Yoda et al.	710	104	
	AI	6,392,653 B1	05/21/02	Malandain et al.	345	501	
	AJ	6,438,668 B1	08/20/02	Esfahani et al.	711	165	
▼	AK	6,636,110 B1	10/21/03	Ooishi et al.	327	565	
SS	AL	6,681,292 B2	01/20/04	Creta et al.	711	119	
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	AS						
	AT						

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AU	"Free On-Line Dictionary of Computing" entry 'Flash Erasable Programmable Read-Only Memory, online May 17, 2004 [http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?flash+memory]
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EXAMINER	DATE CONSIDERED
/Saqib Siddiqui/	06/22/2006

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<b>O I P E</b> INFORMATION DISCLOSURE STATEMENT NOV 29 2004 (use several sheets if necessary)		APPLICANT(S) <b>Joseph M. Jeddelloh</b>			
		FILING DATE <b>September 12, 2003</b>	GROUP ART UNIT <b>2184</b>		

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AA	4,707,823	11/17/87	Holdren et al.	370	1	
	AB	5,241,506	08/31/93	Motegi et al.	365	210	
	AC	5,243,703	09/07/93	Farmwald et al.	395	325	
	AD	5,319,755	06/07/94	Farmwald et a.	395	325	
	AE	5,355,391	10/11/94	Horowitz et al.	375	36	
	AF	5,432,823	07/11/95	Gasbarro et al.	375	356	
	AG	5,465,229	11/07/95	Bechtolsheim et al.	345	477	
	AH	5,502,621	03/26/96	Schumacher et al.	361	760	
	AI	5,566,325	10/15/96	Bruce, II et al.	395	494	
	AJ	5,581,767	12/03/96	Katsuki et al.	395	800	
	AK	5,606,717	02/25/97	Farmwald et al.	395	856	
	AL	5,638,334	06/10/97	Farmwald et al.	365	230.03	
	AM	5,819,304	10/06/98	Nilsen et al.	711	5	
	AN	5,928,343	07/27/99	Farmwald et al.	710	104	
	AO	5,973,935	10/26/99	Schoenfeld et al.	361	813	
	AP	5,973,951	10/26/99	Bechtolsheim et al.	365	52	
	AQ	6,029,250	02/22/00	Keeth	713	400	
	AR	6,061,296	05/09/00	Ternullo, Jr., et al.	365	233	
	AS	6,125,431	09/26/00	Kobayashi	711	154	
	AT	6,175,571 B1	01/16/01	Haddock et al.	370	423	
	AU	6,185,352 B1	02/06/01	Hurley	385	114	
↓	AV	6,247,107 B1	06/12/01	Christie	711	216	
	AW	6,294,937 B1	09/25/01	Crafts et al.	327	158	
SS	AX	6,370,068 B2	04/09/02	Rhee	365	196	
EXAMINER <i>/Saqib Siddiqui/</i>				DATE CONSIDERED <i>06/22/2006</i>			

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<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>				APPLICANT(S) <b>Joseph M. Jeddelloh</b>	
				FILING DATE September 12, 2003	GROUP ART UNIT 2184

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AY	6,401,213 B1	06/04/02	Jeddelloh	713	401	
	AZ	6,430,696 B1	08/06/02	Keeth	713	503	
	BA	6,434,736 B1	08/13/02	Schaecher et al.	716	17	
	BB	6,479,322 B2	11/12/02	Kawata et al.	438	109	
	BC	6,490,188 B2	12/03/02	Nuxoll et al.	365	63	
	BD	6,629,220 B1	09/30/03	Dyer	711	158	
	BE	6,646,929 B1	11/11/03	Moss et al.	365	194	
	BF	6,662,304 B2	12/09/03	Keeth et al.	713	400	
	BG	6,667,895 B2	12/23/03	Jang et al.	365	63	
	BH	6,721,195 B2	04/13/04	Brunelle et al.	365	63	
	BI	6,724,685 B2	04/20/04	Braun et al.	365	233	
	BJ	6,735,679 B1	05/11/04	Herbst et al.	711	167	
	BK	6,745,275 B2	06/01/04	Chang	710	305	
	BL	6,751,703 B2	06/15/04	Chilton	711	113	
	BM	6,756,661 B2	06/29/04	Tsuneda et al.	257	673	
	BN	6,771,538 B2	08/03/04	Shukuri et al.	365	185.05	
	BO	6,792,059 B2	09/14/04	Yuan et al.	375	354	
	BP	6,799,246 B1	09/28/04	Wise et al.	711	117	
	BQ	6,804,764 B2	10/12/04	LaBerge et al.	711	170	
	BR	6,816,947 B1	11/09/04	Huffman	711	151	
	BS	2003/0043158 A1	03/06/03	Wasserman et al.	345	545	
	BT	2003/0093630 A1	05/15/03	Richard et al.	711	154	
▼	BU	2003/0163649 A1	08/28/03	Kapur et al.	711	146	
SS	BV	2003/0177320 A1	09/18/03	Sah et al.	711	158	

EXAMINER <i>/Saqib Siddiqui/</i>	DATE CONSIDERED 06/22/2006
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<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>				APPLICANT(S) <b>Joseph M. Jeddelloh</b>	
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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	BW	2003/0193927 A1	10/16/03	Hronik	370	351	
SS	BX	2004/0022094 A1	02/05/04	Radhakrishnan et al.	365	200	

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
SS	BY	WO 93/19422	09/30/93	PCT			YES      NO
SS	BZ	0 849 685 A2	06/24/98	EP			

**OTHER PRIOR ART** *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EXAMINER	DATE CONSIDERED
CA <i>/Saqib Siddiqui/</i>	06/22/2006

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INFORMATION DISCLOSURE STATEMENT <small>(Checkmarks if necessary)</small>		APR 01 2005 JC		APPLICANT(S) Joseph M. Jeddeloh	
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SS	AA	4,045,781	08/30/77	Levy et al.	364	200	
	AB	4,240,143	12/16/80	Besemer et al.	364	200	
	AC	4,245,306	01/13/81	Besemer et al.	364	200	
	AD	4,253,144	02/24/81	Bellamy et al.	364	200	
	AE	4,253,146	02/24/81	Bellamy et al.	364	200	
	AF	4,724,520	02/09/88	Athanas et al.	364	200	
	AG	4,930,128	05/29/90	Suzuki et al.	371	12	
	AH	5,269,022	12/07/93	Shinjo et al.	395	700	
	AI	5,317,752	05/31/94	Jewett et al.	395	750	
	AJ	5,461,627	10/24/95	Rypinski	370	95.2	
	AK	5,479,370	12/26/95	Furuyama et al.	365	189.12	
	AL	5,577,220	11/19/96	Combs et al.	395	416	
	AM	5,715,456	02/03/98	Bennett et al.	395	652	
	AN	5,729,709	03/17/98	Harness	395	405	
	AO	5,832,250	11/03/98	Whittaker	395	471	
	AP	5,966,724	10/12/99	Ryan	711	105	
	AQ	5,978,567	11/02/99	Rebane et al.	395	200.49	
	AR	6,023,726	02/08/00	Saksena	709	219	
	AS	6,076,139	06/13/00	Welker et al.	711	104	
	AT	6,098,158	08/01/00	Lay et al.	711	162	
▼	AU	6,134,624	10/17/00	Burns et al.	710	131	
	AV	6,191,663 B1	02/20/01	Hannah	333	17.3	
SS	AW	6,249,802 B1	06/19/01	Richardson et al.	709	200	

EXAMINER /Saqib Siddiqui/ DATE CONSIDERED

06/22/2006

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				FILING DATE <b>September 12, 2003</b>	GROUP ART UNIT <b>2184</b>

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AX	6,405,280 B1	06/11/02	Ryan	711	105	
	AY	6,434,696 B1	08/13/02	Kang	713	2	
	AZ	6,438,622 B1	08/20/02	Haghghi et al.	710	1	
	BA	6,449,308 B1	09/10/02	Knight, Jr. et al.	375	212	
	BB	6,453,393 B1	09/17/02	Holman et al.	711	154	
	BC	6,501,471 B1	12/31/02	Venkataraman et al.	345	424	
	BD	6,594,722 B1	07/15/03	Willke, II et al.	710	313	
	BE	6,598,154 B1	07/22/03	Vaid et al.	712	237	
	BF	6,615,325 B2	09/02/03	Mailloux et al.	711	154	
	BG	6,628,294 B1	09/30/03	Sadowsky et al.	345	568	
	BH	6,658,509 B1	12/02/03	Bonella et al.	710	100	
	BI	6,697,926 B2	02/24/04	Johnson et al.	711	167	
	BJ	6,718,440 B2	04/06/04	Maiyuran et al.	711	137	
	BK	6,728,800 B1	04/27/04	Lee et al.	710	54	
	BL	6,735,682 B2	05/11/04	Segelken et al.	711	220	
	BM	6,754,812 B1	06/22/04	Abdallah et al.	712	234	
	BN	6,760,833 B1	07/06/04	Dowling	712	34	
	BO	6,775,747 B2	08/10/04	Venkatraman	711	137	
	BP	6,789,173 B1	09/07/04	Tanaka et al.	711	147	
	BQ	6,792,496 B2	09/14/04	Aboulenein et al.	710	306	
	BR	6,795,899 B2	09/21/04	Dodd et al.	711	137	
	BS	6,799,268 B1	09/28/04	Boggs et al.	712	228	
	BT	6,804,760 B2	10/12/04	Wiliams	711	170	
	BU	6,807,630 B2	10/19/04	Lay et al.	713	2	
SS	BV	6,820,181 B2	11/16/04	Jeddeloh et al.	711	169	

EXAMINER	DATE CONSIDERED	
/Saqib Siddiqui/		06/22/2006

\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

FORM PTO-1449 (REV.7-80)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 501321.01	APPLICATION NO. 10/660,844
<b>INFORMATION DISCLOSURE STATEMENT</b> <i>(Use several sheets if necessary)</i>				APPLICANT(S) <b>Joseph M. Jeddelloh</b>	
				FILING DATE September 12, 2003	GROUP ART UNIT 2184

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	BW	6,823,023 B1	11/23/04	Hannah	375	296	
SS	BX	2001/0039612 A1	11/08/01	Lee	713	2	
SS	BY	2002/0116588 A1	08/22/02	Beckert et al.	711	161	
	BZ						

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
SS	CA	WO 02/27499 A2	04/04/02	PCT				
	CB							
	CC							

**OTHER PRIOR ART** (*Including Author, Title, Date, Pertinent Pages, Etc.*)

SS	CD	Intel, "Flash Memory PCI Add-In Card for Embedded Systems", Application Note AP-758, September, 1997, pp. i-13.
SS	CE	Shanley, T. et al., "PCI System Architecture", Third Edition, Mindshare, Inc., 1995, pp. 24-25.
	CF	
	CG	
	CH	
	CI	
	CJ	

EXAMINER	/Saqib Siddiqui/	DATE CONSIDERED
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INFORMATION DISCLOSURE STATEMENT <small>(Additional sheets if necessary)</small>		APPLICANT(S) Joseph M. Jeddelloh			
		FILING DATE September 12, 2003	GROUP ART UNIT 2184		

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SS	AA	5,274,584	12/28/93	Henderson et al.	365	64	
	AB	5,450,578	09/12/95	Mackenthun	395	182.02	
	AC	5,619,676	04/08/97	Fukuda et al.			
	AD	6,970,968 B1	11/29/05	Holman	711	5	
	AE	2002/0089925 A1	07/11/02	Smith et al.	370	216	
	AF	2003/0056038 A1	03/20/03	Cochran	710	36	
▼	AG	2004/0123043 A1	06/24/04	Rotithor et al.	711	137	
SS	AH	2004/0123180 A1	06/24/04	Soejima et al.	714	5	
	AI						
	AJ						
	AK						
	AL						
	AM						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AN							
	AO							
	AP							

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AQ	
AR	

EXAMINER  /Saqib Siddiqui/	DATE CONSIDERED  06/22/2006
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